



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi**  
**HASHIMOTO et al.**

Group Art Unit: **2814**

Serial No.: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

Confirmation No.: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A  
SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD  
CHARACTERISTIC (AS AMENDED)**

Attorney Docket Number: **020244**

Customer Number: **38834**

**SUBMISSION UNDER 37 C.F.R. §1.114**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

December 27, 2004

Sir:

This Submission is being filed concurrently with a Request for Continued Examination pursuant to 37 C.F.R. §1.114.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 16 of this paper.